

Prior Anti-Parallel VHDs Communication

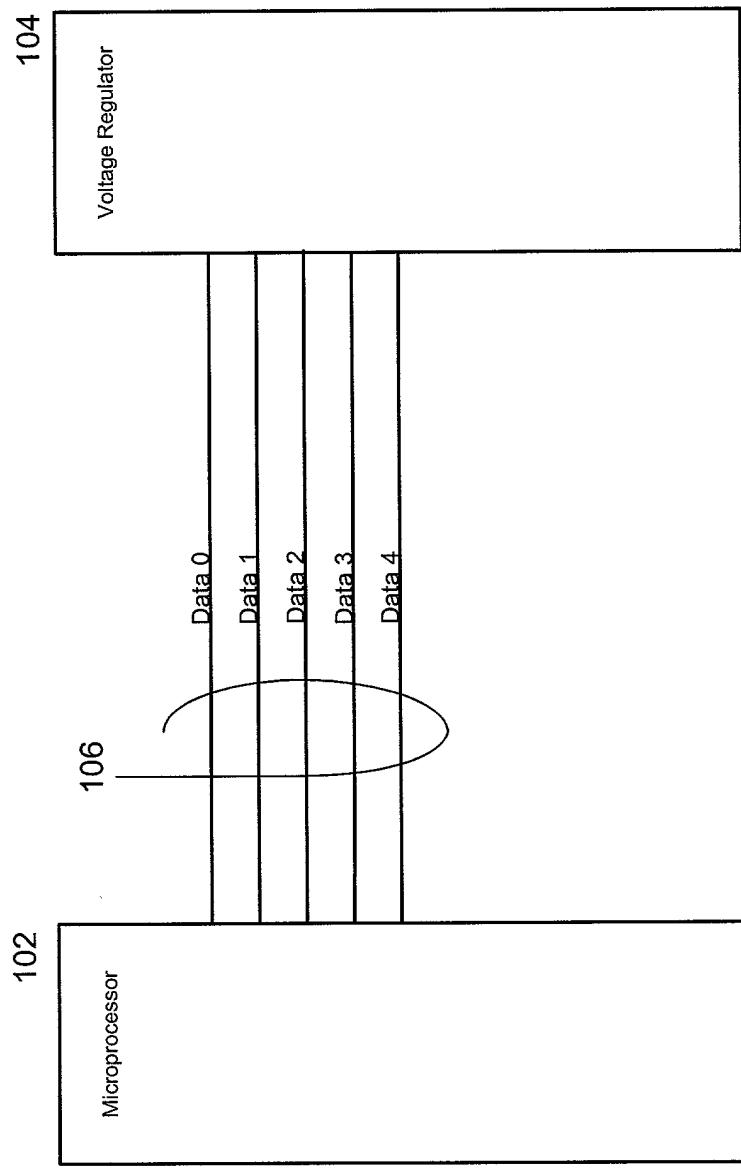


Figure 1

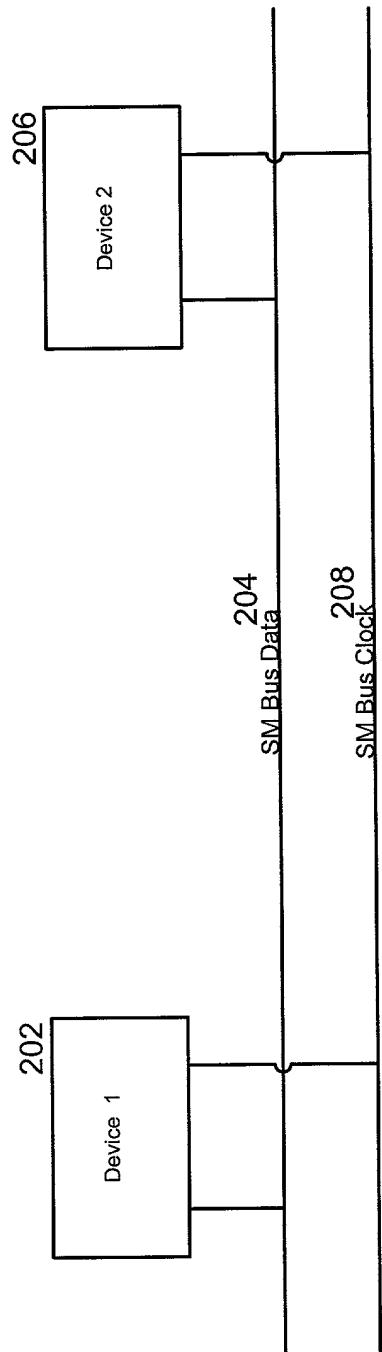


Figure 2

TCI Serial VID Interface

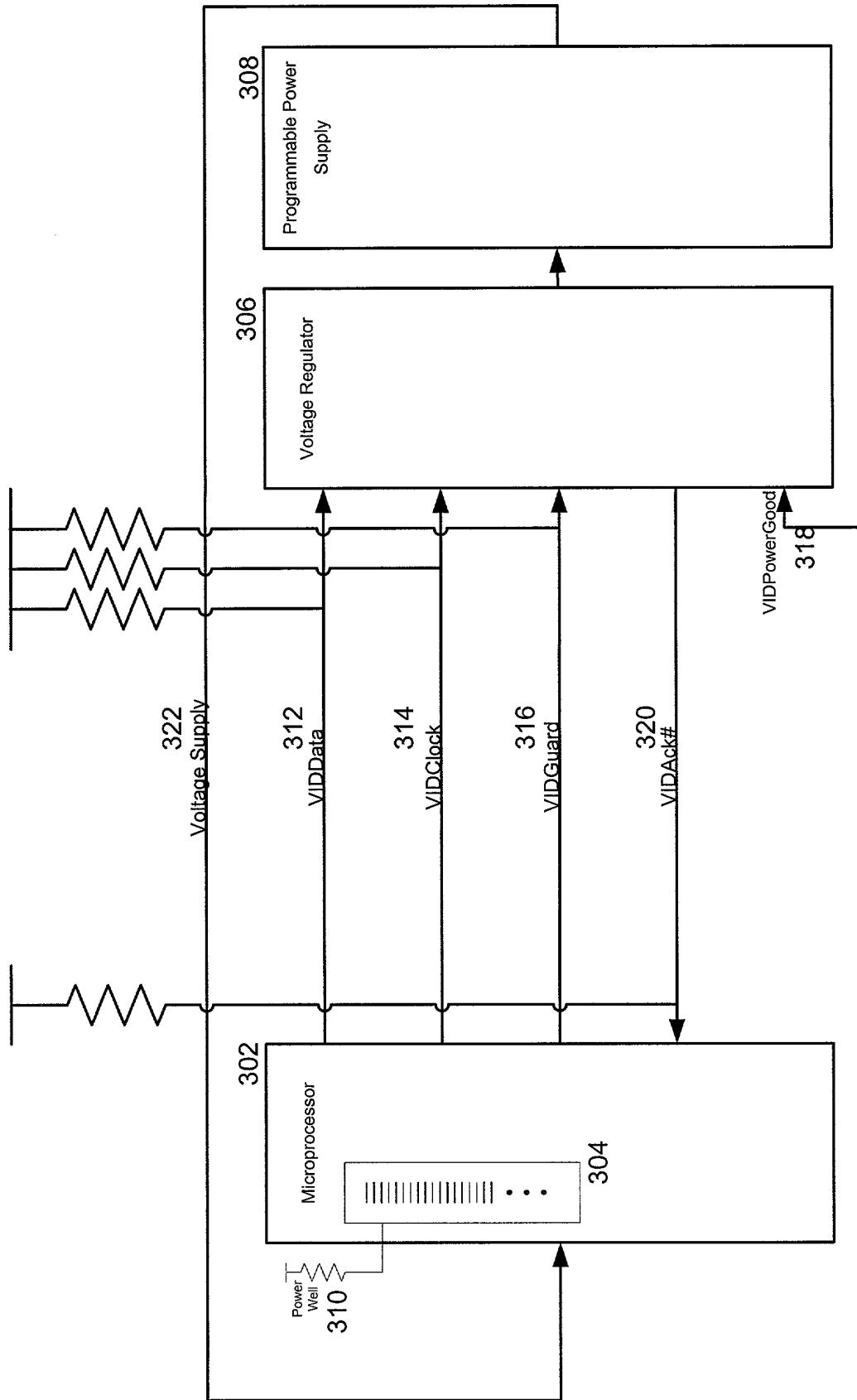


Figure 3

VIDData and VIDGuard Timing

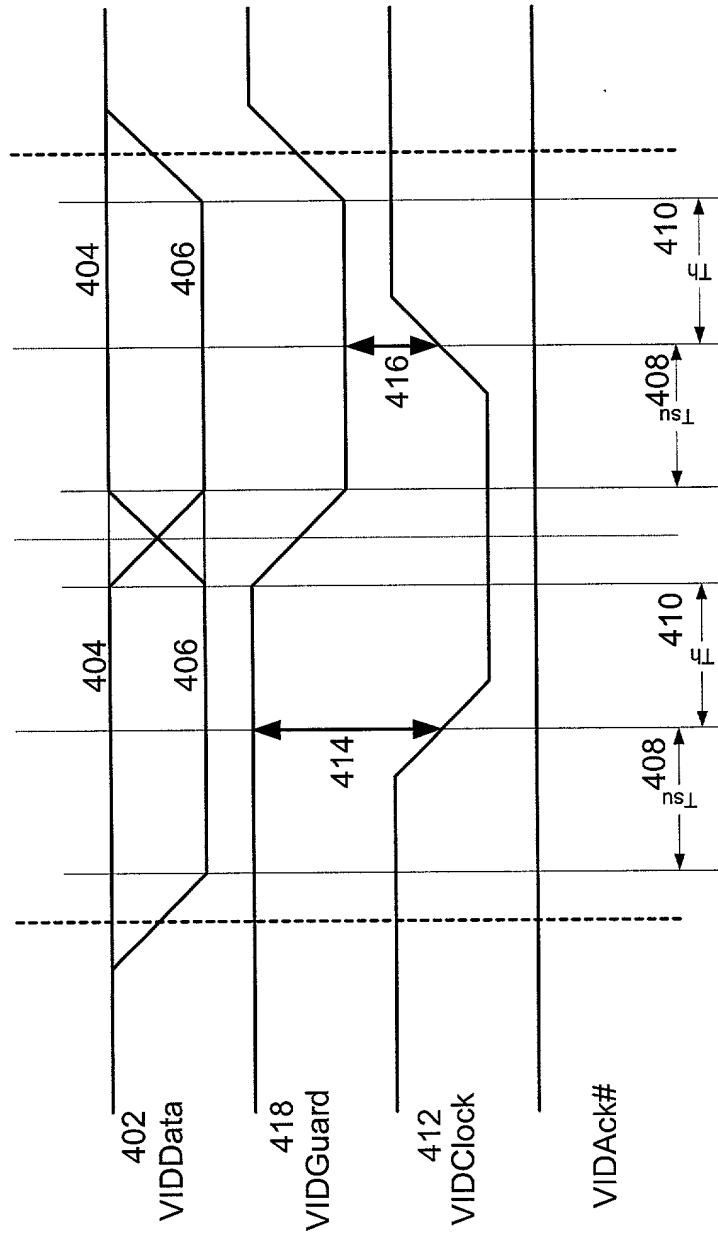


Figure 4

VIDAck# Timing

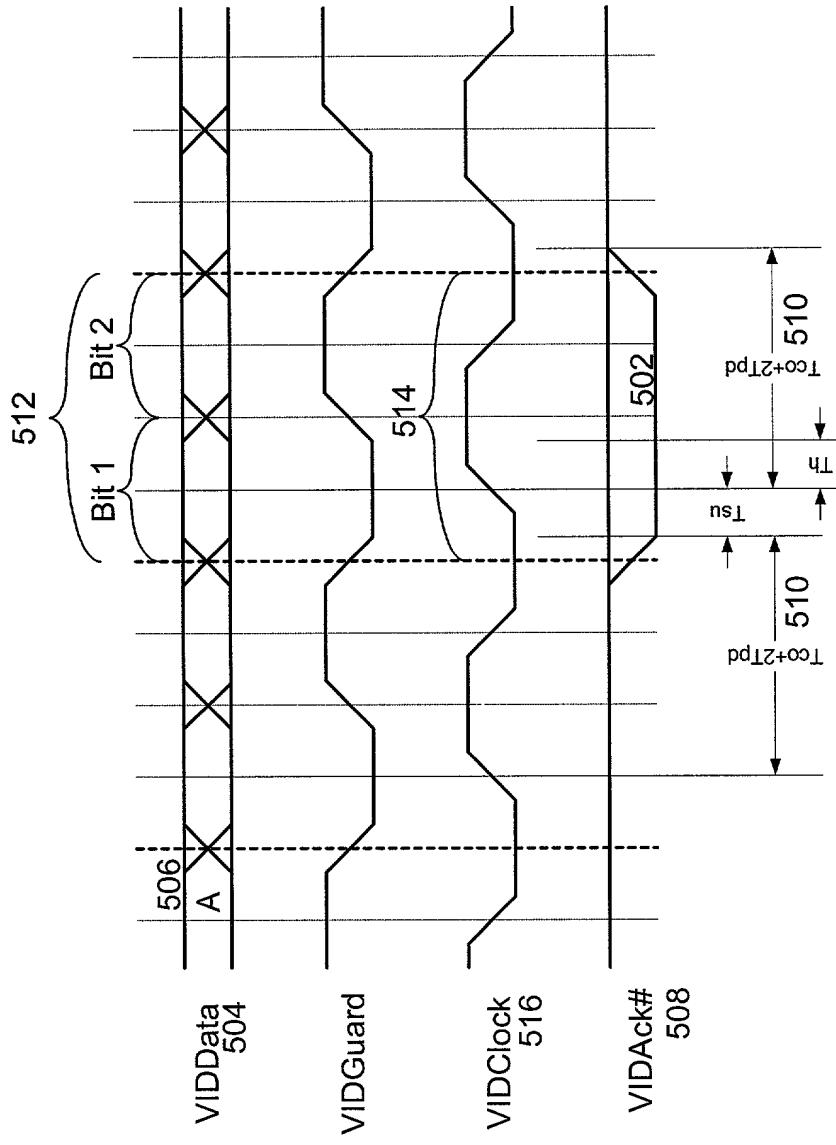


Figure 5

TCU604 Prior Art: SMBus

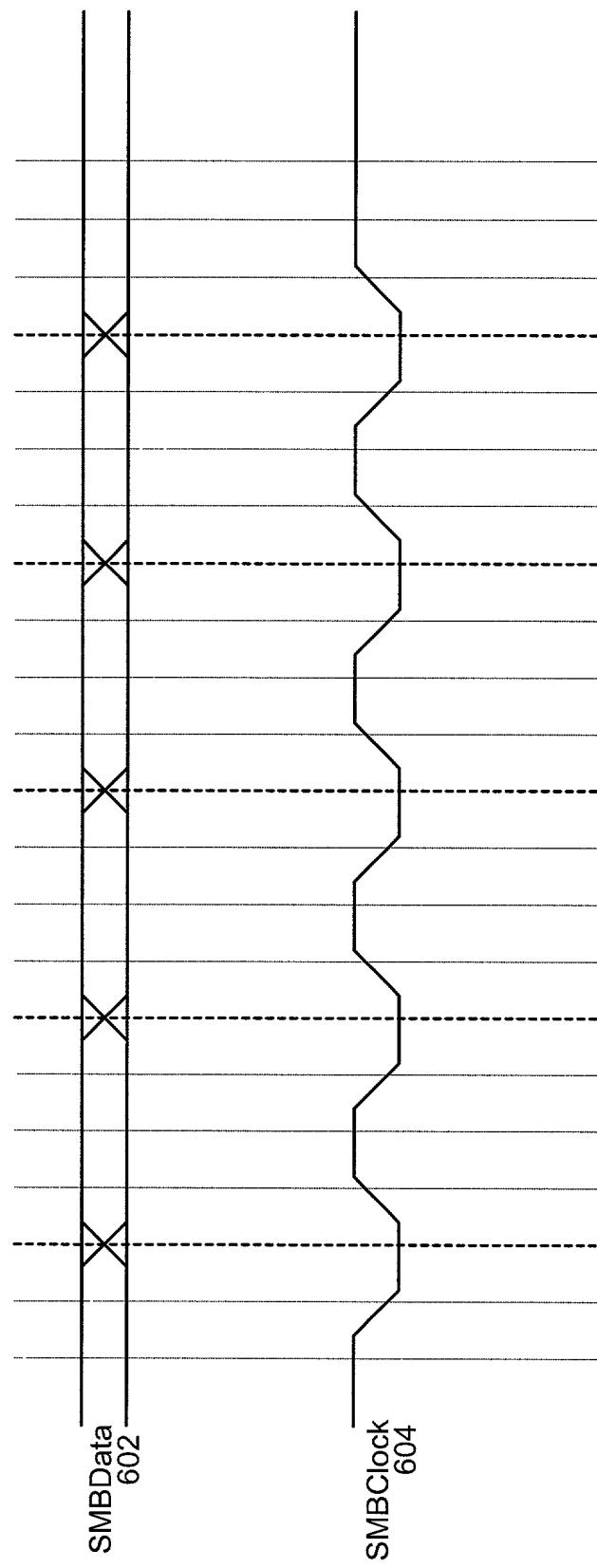


Figure 6

VIDClock Command Byte

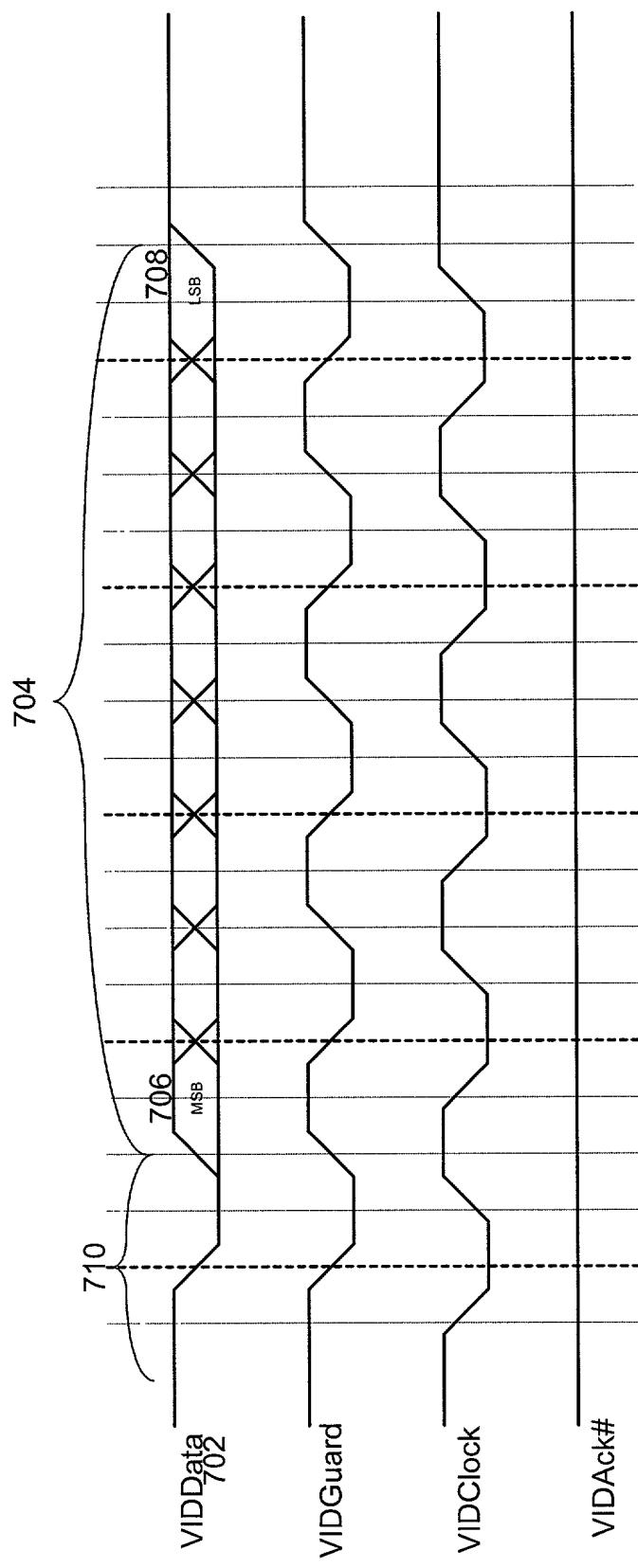


Figure 7

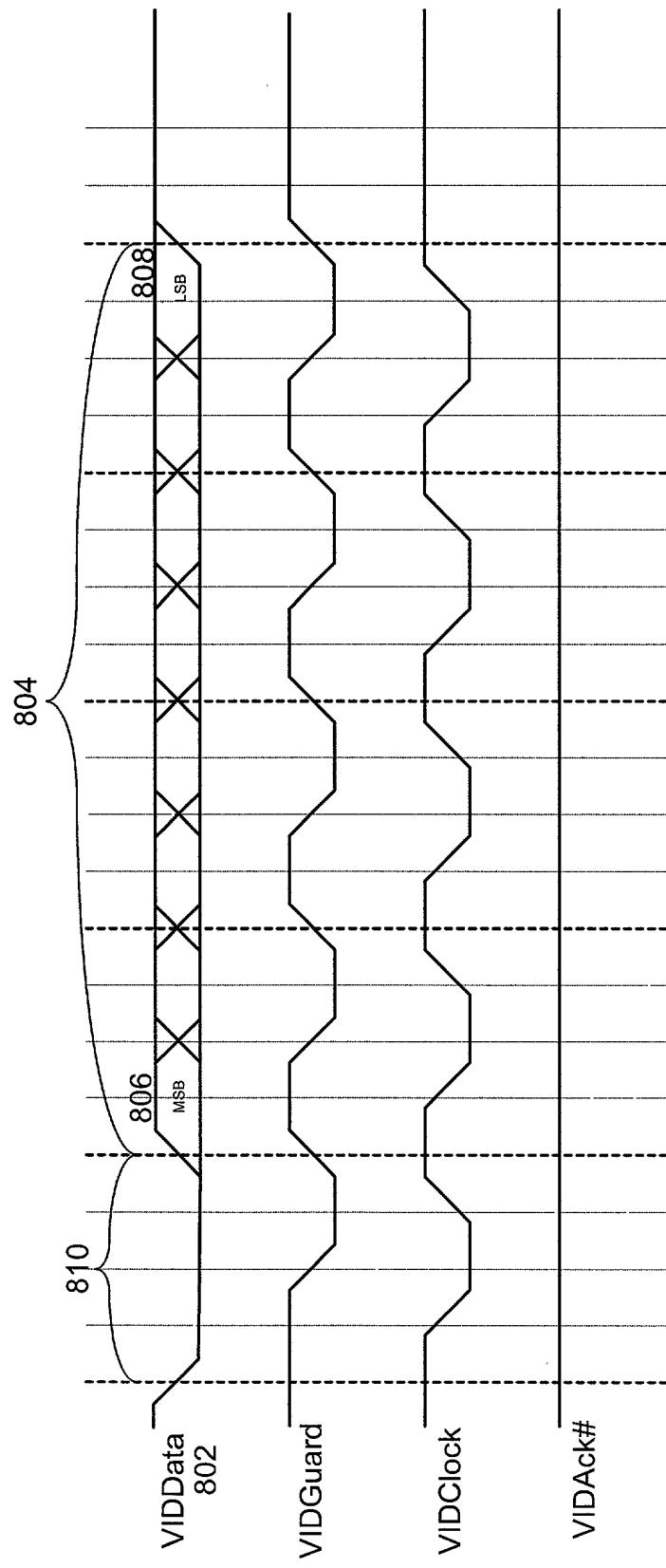
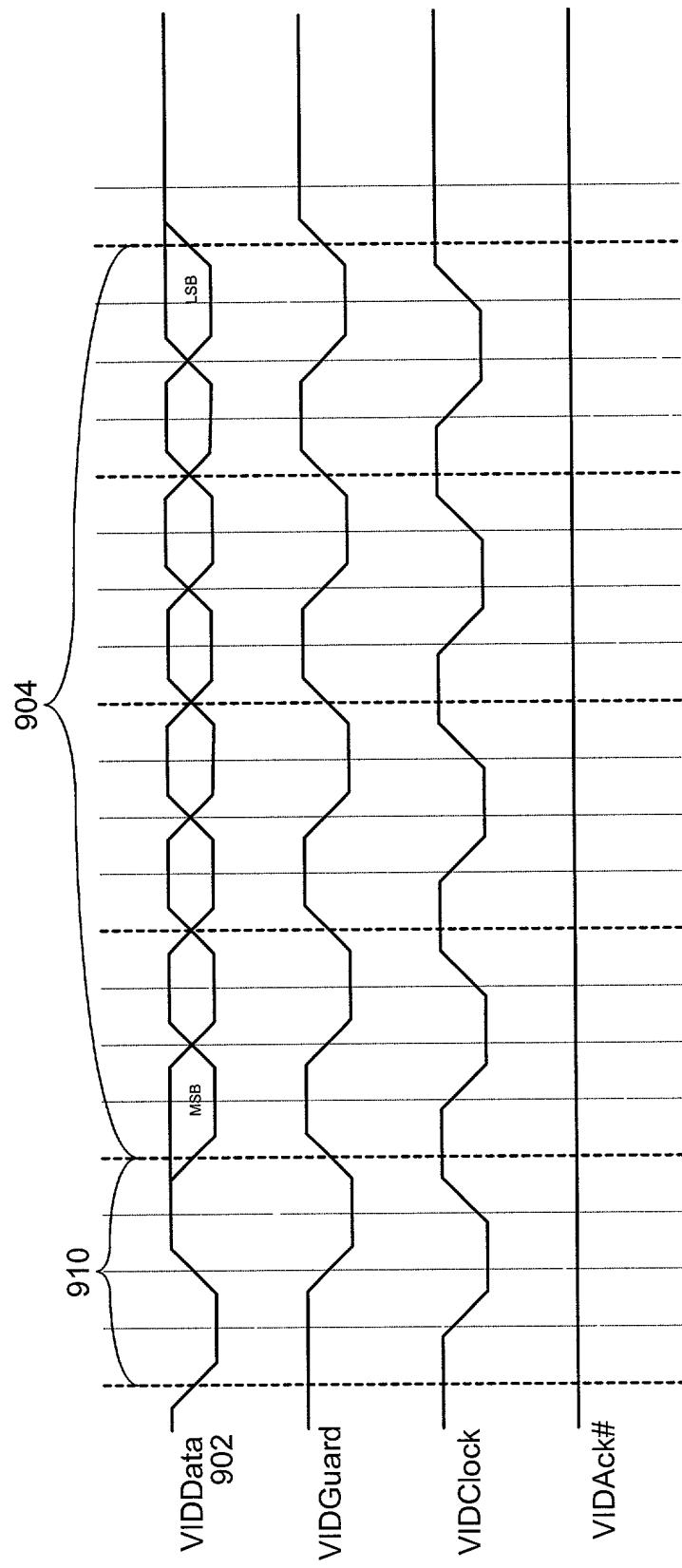


Figure 8



Ack Byte with Positive Acknowledgement

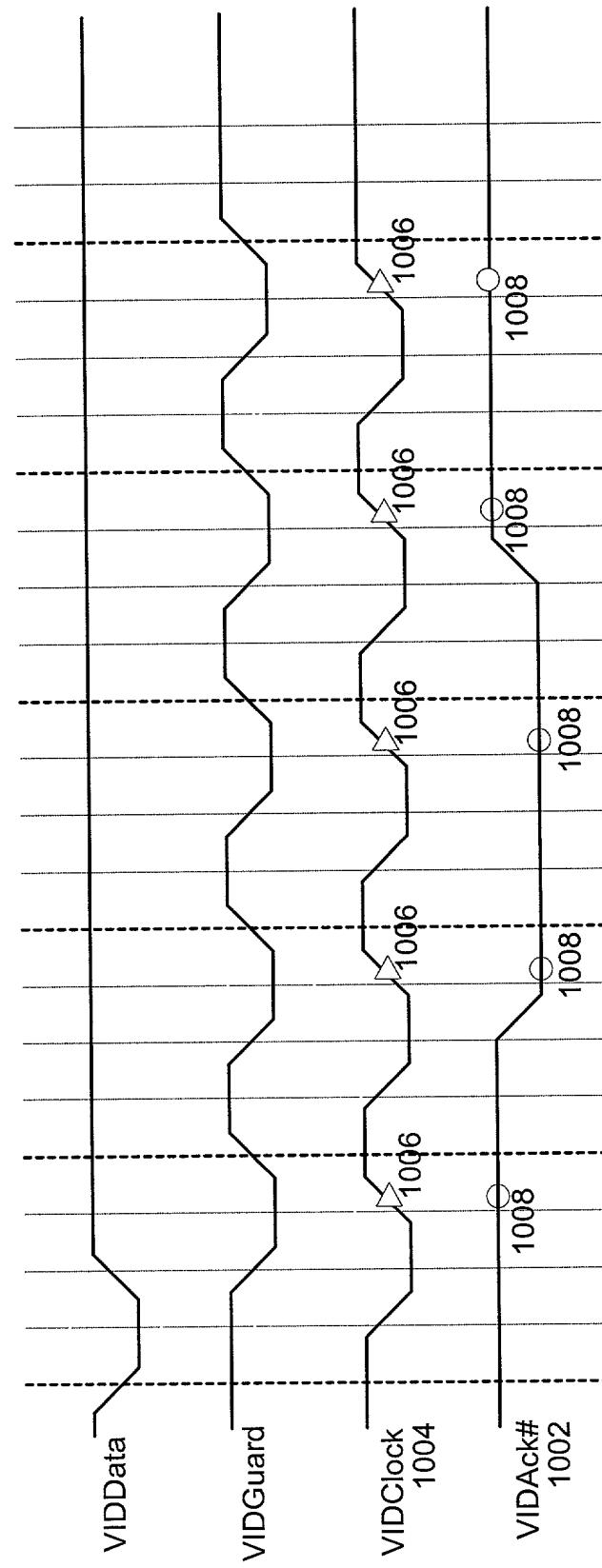


Figure 10

VIDData₁₁₀₂ Sync Byte VIDClock VIDAck#

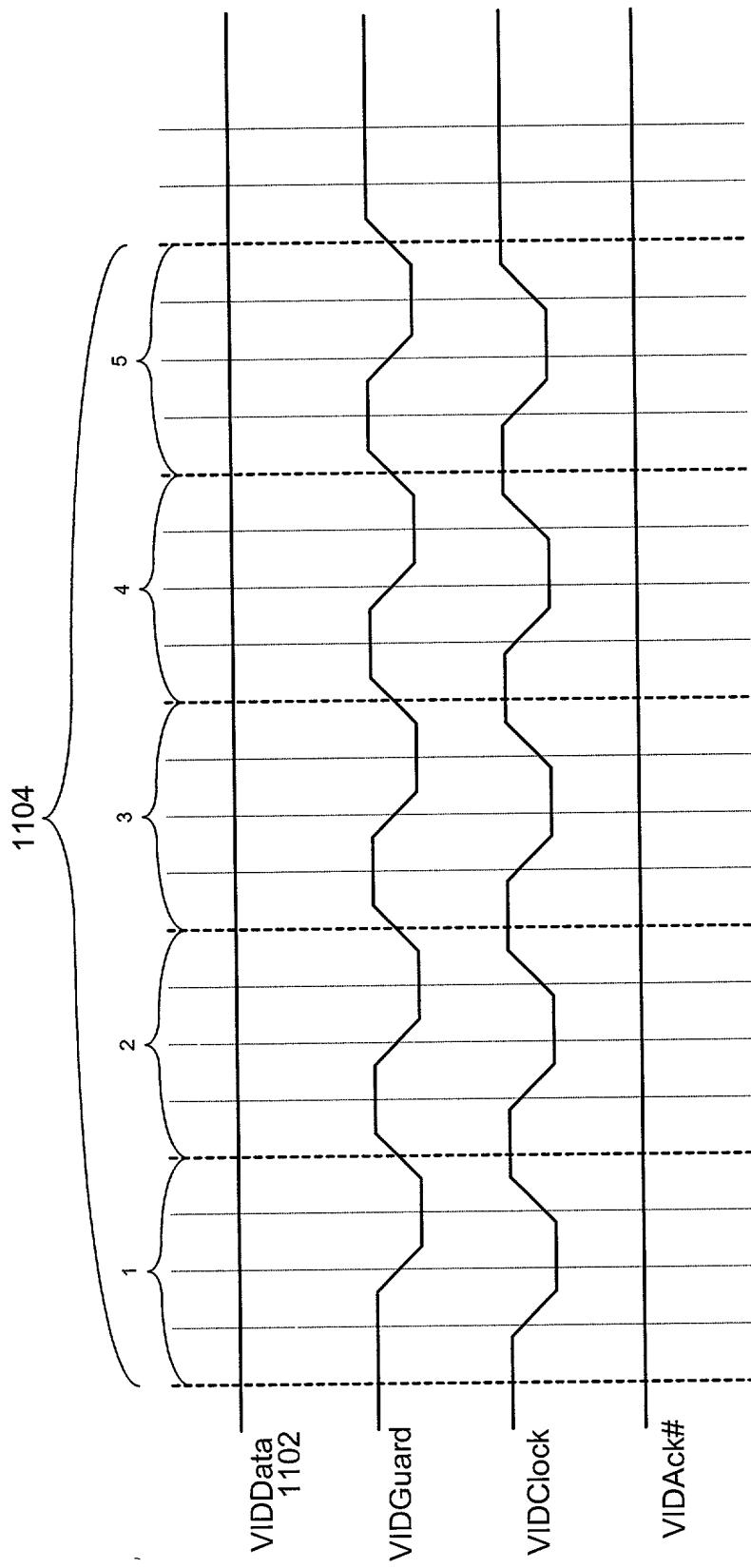


Figure 11

Complete Command

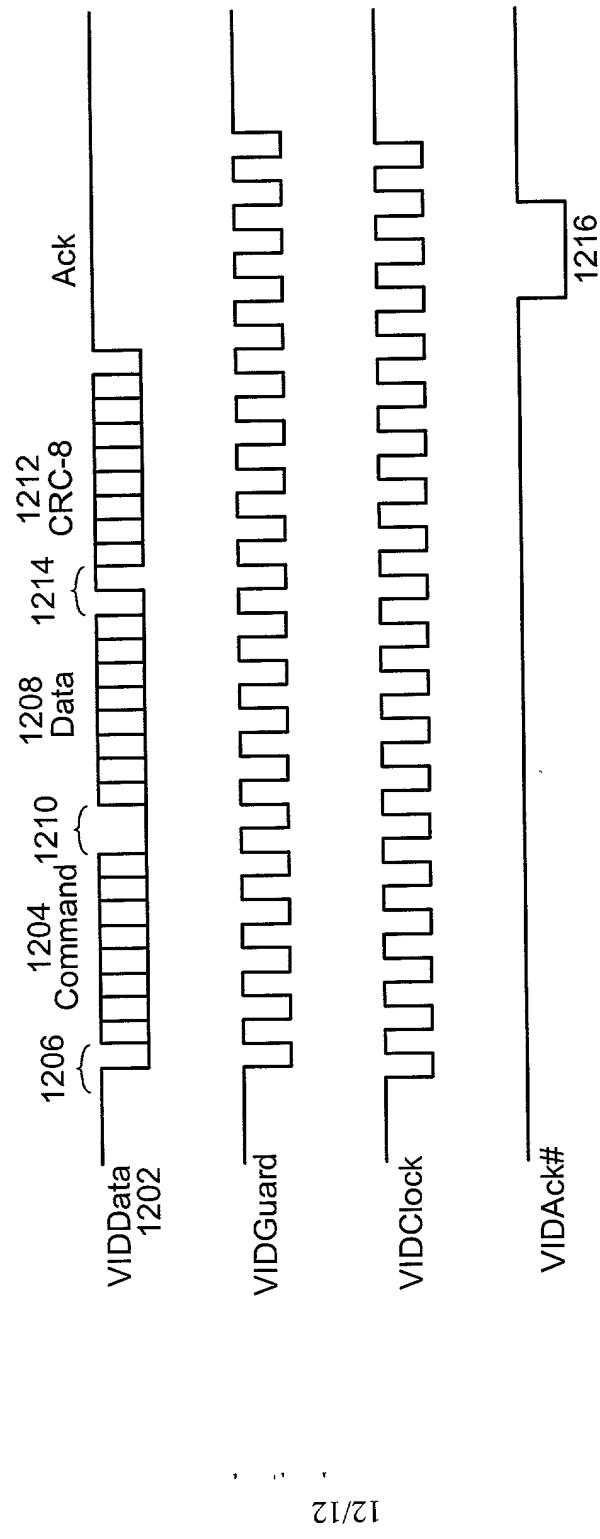


Figure 12